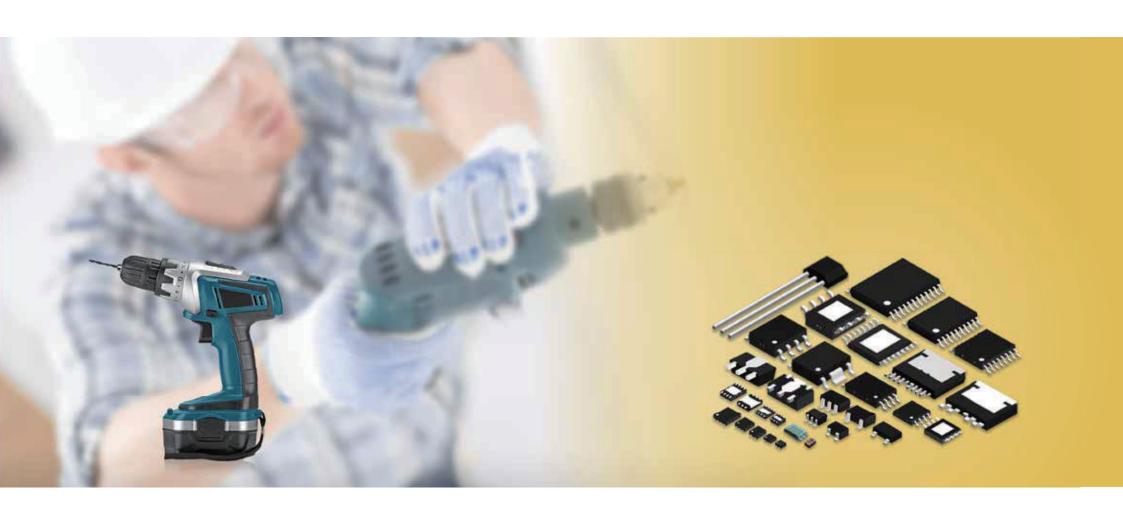


# **Product Catalog**

Sensors 2022



**ABLIC Inc.** 

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### S-5420

#### ULTRAVIOLET LIGHT SENSOR, UV-A to UV-B SENSING SI PHOTODIODE

### Features

Wide-range sensitivity wavelength:

High-sensitivity ultraviolet light detection:

• Ultraviolet components detection:

• Lead-free, halogen-free

 $\lambda$  = 250 nm to 1000 nm

 $S_H = 0.17 \text{ A/W } (\lambda = 365 \text{ nm})$ 

As a result of the difference of output between two types of photodiodes

(Requirement: external parts)

SON-6C

1 GND 2 NC 3 NC 4 ILOUT

5 NC 6 IHOUT

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### S-5852A Series

#### HIGH-ACCURACY DIGITAL TEMPERATURE SENSOR WITH THERMOSTAT FUNCTION

#### Features

• Temperature accuracy, high-accuracy temperature range\*1: ±0.5°C typ. /±1.0°C max. (Ta = 0°C to +65°C)

Temperature resolution:

(Selectable by the resolution register)

• Temperature sample rate:

Hvsteresis width:

(Selectable by the configuration register)

· Current consumption:

Shutdown mode at serial bus non-active:

Active mode at serial bus non-active:

Operation voltage range:

Operation frequency:

Thermostat function:

Noise suppression:

· Operation temperature range:

• Lead-free (Sn 100%), halogen-free

 $\pm 0.5$ °C typ. /  $\pm 1.0$ °C max. (Ta = +75°C to +95°C)

0.5°C, 0.25°C, 0.125°C, 0.0625°C

7 samples / s min.

No hysteresis, 1.5°C, 3.0°C, 6.0°C

 $I_{DD3} = 0.3 \mu A \text{ typ.}, I_{DD3} = 3.0 \mu A \text{ max.}$  $I_{DD1} = 40.0 \mu A \text{ typ.}, I_{DD1} = 100.0 \mu A \text{ max.}$ 

1.7 V to 3.6 V

1.0 MHz max.  $(V_{DD} = 2.2 \text{ V to } 3.6 \text{ V})$ 

400 kHz max.  $(V_{DD} = 1.7 \text{ V to } 2.2 \text{ V})$ 

Dual trip mode, single trip mode

(Selectable by the configuration register)

Schmitt trigger and noise filter on input pins (SCL, SDA)

 $Ta = -40^{\circ}C \text{ to } +125^{\circ}C$ 

\*1. The option of the high-accuracy temperature range can be selected.



### S-5851A Series

#### 2-WIRE DIGITAL TEMPERATURE SENSOR

#### Features

• Low voltage operation  $: V_{DD} (min.) = 2.7 V$ 

• Low current consumption

: 45 μA typ. (+25°C) 1 μA typ. (+25°C at shutdown)

• High accuracy : ±2.0°C (max.) -25°C to +85°C

 $\pm 3.0$ °C (max.) -40°C to +125°C

• Temperature resolution : 0.0625°C

: 2-wire serial interface Digital output

· Maximum operating frequency

: 400 kHz

· Low power supply voltage detection circuit Lead-free, Sn 100%, halogen-free\*1

#### \*1. Refer to " Product Name Structure" for details.

SOT-23-6		SNT-6A
6 5 4 B B B	1 SCL 2 VSS 3 AD1 4 VDD 5 AD0	1 AD 2 VS 1 0 6 3 SC 3 4 4 SD 5 AD
1 2 3	6 SDA	6 VD

### S-58LM20A Series

#### **CMOS TEMPERATURE SENSOR IC**

#### Features

· Accuracy against temperature ±2.5°C (-55°C to +130°C) Linear output voltage -11.77 mV/°C Typ.

 $Ta = -30^{\circ}C$ : 2.205 V Typ.

 $Ta = +30^{\circ}C$ : 1.515 V Typ.  $Ta = +130^{\circ}C : 0.303 \text{ V Typ.}$ 

 Nonlinearity • Operation in wide range of power supply voltage  $\pm 0.4\%$  Typ. (-20 to +80°C)  $V_{DD}$  = 2.4 to 5.5 V (-30°C to +130°C)

 $V_{DD}$  = 2.7 to 5.5 V (-55°C to +130°C)

· Low current consumption

· Built-in operational amplifier

 Output voltage referred to V<sub>SS</sub> • Lead-free, Sn 100%, halogen-free\*1 4.5 μA Typ. (+25°C) 6.0 μA Max. (-55°C to +130°C)

\*1. Refer to "■ Product Name Structure" for details

SC-82AB 1 VDD 2 VSS 3 NC 4 VOUT

SNT-4A

1 VSS 2 VDD 3 VOUT 4 NC

VDD 2 VSS 3 VSS 4 VOUT

WLP-4B

### S-8110C/8120C Series

#### **CMOS TEMPERATURE SENSOR IC**

#### Features

 Temperature accuracy S-8110C Series: ±5.0°C (-30°C to +100°C)

S-8120C Series: ±2.5°C (-30°C to +100°C)

 Linear output voltage -8.20 mV/°C tvp.

> $Ta = -30^{\circ}C$ : 1.951 V tvp.  $Ta = +30^{\circ}C$ : 1.474 V typ.

Ta = +100°C: 0.882 V typ.

 Nonlinearity  $\pm 0.5\%$  typ. (-20°C to +80°C)

• Wide power supply voltage operation  $V_{DD} = 2.4 \text{ V}$  to 10.0 V

 Low current consumption 4.5 uA tvp. (+25°C)

Built-in operational amplifier

V<sub>SS</sub> standard output

Lead-free, Sn 100%, halogen-free\*1

#### \*1. Refer to "■ Product Name Structure" for details.



4 VOUT

### S-5813A/5814A Series

#### Features

Nonlinearity

 Temperature accuracy S-5813A Series: ±5.0°C (-30°C to +100°C) S-5814A Series: ±2.5°C (-30°C to +100°C)

· Linear output voltage

• Wide power supply voltage operation

Low current consumption

· Built-in operational amplifier

Output voltage referred to V<sub>SS</sub>

• Lead-free (Sn 100%), halogen-free

#### **CMOS TEMPERATURE SENSOR IC**

-11.04 mV/°C typ. Ta =  $-30^{\circ}$ C: 2.582 V typ.  $Ta = +30^{\circ}C$ : 1.940 V typ.  $Ta = +100^{\circ}C$ : 1.145 V typ.  $\pm 0.5\%$  typ. (-20°C to +80°C)  $V_{DD} = 2.4 \text{ V to } 10.0 \text{ V } (+25^{\circ}\text{C})$ 

SNT-4A

2 VDD 3 VOUT

4.0 μA typ. (+25°C)

### S-5844A Series

#### **TEMPERATURE SWITCH IC (THERMOSTAT IC)**

#### Features

· Detection temperature:  $T_{DET} = +50^{\circ}C$  to  $+100^{\circ}C$ ,  $+5^{\circ}C$  step, detection accuracy:  $\pm 2.5^{\circ}C$ 

· Low voltage operation:  $V_{DD} = 1.65 \text{ V min.}$ 

· Low current consumption:  $I_{DD} = 0.18 \,\mu\text{A typ.}$  (Ta = +25°C) selectable in 5°C, 10°C, 15°C or 20°C Hysteresis temperature:

• Selectable output logic in active "H" or active "L"

• Selectable output form in CMOS or Nch open-drain

 Operation temperature range:  $Ta = -40^{\circ}C \text{ to } +125^{\circ}C$ 

• Lead-free (Sn 100%), halogen-free

SOT-23-5



1 TEST 2 VSS 3 DET 4 NC 5 VDD

1 DET 2 VSS 3 TEST 4 VDD

SNT-4A

1 DET 2 VSS 3 TEST

HSNT-4(1010)

### S-5841 Series

#### **TEMPERATURE SWITCH IC (THERMOSTAT IC)**

#### Features

Detection temperature:  $T_{DFT}$  = +40°C to +100°C, +1°C step, detection accuracy:  $\pm 2.5$ °C

Low voltage operation:  $V_{DD} = 2.2 \text{ V min.}$ 

(Detection temperature =  $+55^{\circ}$ C to  $+100^{\circ}$ C, Ta =  $-40^{\circ}$ C to  $+100^{\circ}$ C)

 $I_{DD} = 10 \mu A \text{ typ. (Ta} = +25 ^{\circ}\text{C})$ Low current consumption:

Hysteresis temperature can be switched in 0°C, 2°C, 4°C and 10°C.

Selectable output logic in active "H" or "L"

Selectable output form in CMOS or Nch open drain

Operation temperature range:  $Ta = -40^{\circ}C \text{ to } +125^{\circ}C$ 

Lead-free, Sn 100%, halogen-free\*1

#### \*1. Refer to "■ Product Name Structure" for details.



### S-585AA

BUILT-IN ARP FUNCTION 2-WIRE SERIAL E<sup>2</sup>PROM WITH TEMPERATURE SENSOR

#### Features

#### E<sup>2</sup>PROM block

• Page write: 16 bytes / page

• Sequential read

• Write protect function during low power supply voltage

• Write protect:

 $\label{eq:continuous} \begin{tabular}{ll} Individual software data protection for each of four 128-byte blocks \\ \bullet Endurance: $10^6$ cycle / word*1 (Ta = +25°C) \\ \end{tabular}$ 

• Data retention: 100 years (Ta =  $+25^{\circ}$ C)

Memory capacity: 4 K-bit Initial delivery state: FFh

#### Temperature sensor block

• Temperature accuracy:  $\pm 0.25^{\circ}$ C typ.  $/ \pm 1.0^{\circ}$ C max. (Ta = 0°C to +85°C)

 $\pm 0.25$ °C typ.  $/ \pm 1.5$ °C max. (Ta = -40°C to +125°C)

• Temperature sample rate: 8 samples / s min.

• Selectable hysteresis width: No hysteresis, 1.5°C, 3.0°C, 6.0°C

#### Overall

- Support for SMBus ARP function
- Support for Alert Response Address function (ARA)
- Support for Default Slave Address (DSA)
- Current consumption:

E²PROM in standby mode and temperature sensor in shutdown mode:  $3.0~\mu A$  max. E²PROM in standby mode and temperature sensor in active mode: 0.1~mA max. E²PROM in read operation mode and temperature sensor in active mode: 0.4~mA max. E²PROM in write operation mode and temperature sensor in active mode: 2.0~mA max.

• Operation voltage range: 1.7 V to 3.6 V

• Operation frequency: 1.0 MHz max.  $(V_{DD} = 2.2 \text{ V to } 3.6 \text{ V})$ 

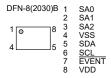
400 kHz max.  $(V_{DD} = 1.7 \text{ V to } 3.6 \text{ V})$ 

• Noise suppression: Schmitt trigger and noise filter on input pins (SCL, SDA)

• Operation temperature range: Ta = -40°C to +125°C

• Lead-free (Sn 100%), halogen-free

\*1. For each address (Word: 8-bit)



**ABLIC Inc.** 

### S-5718 Series

LOW VOLTAGE OPERATION **OMNIPOLAR / UNIPOLAR DETECTION TYPE** HALL EFFECT SWITCH IC WITH SWITCHABLE DETECTION POLE FUNCTION

#### Features

• Switchable detection pole function: Omnipolar detection, S pole detection, N pole detection

 Output logic\*1: Active "L" Active "H"

• Output form: CMOS output

Magnetic sensitivity (hysteresis width)\*1:

 $B_{OP}$  = 1.8 mT typ. ( $B_{HYS}$  = 0.7 mT typ.)  $B_{OP} = 3.0 \text{ mT typ.} (B_{HYS} = 0.8 \text{ mT typ.})$  $B_{OP}$  = 3.0 mT typ. ( $B_{HYS}$  = 1.3 mT typ.)  $B_{OP} = 4.5 \text{ mT typ.} (B_{HYS} = 1.0 \text{ mT typ.})$  $B_{OP} = 4.5 \text{ mT typ.} (B_{HYS} = 2.5 \text{ mT typ.})$ 

Operating cycle (current consumption)\*1:

 $t_{CYCLE}$  = 102.1 ms typ. ( $I_{DD}$  = 1.4  $\mu$ A typ.)  $t_{CYCLE}$  = 50.5 ms typ. ( $I_{DD}$  = 2.0  $\mu$ A typ.)  $t_{CYCLE} = 5.7 \text{ ms typ.} (I_{DD} = 12.0 \mu A \text{ typ.})$  $V_{DD} = 1.45 \text{ V to } 3.6 \text{ V}$ Power supply voltage range\*2

· Operation temperature range:

 $Ta = -40^{\circ}C \text{ to } +85^{\circ}C$ 

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.

\*2. Power supply voltage range is different by optional combination.

#### SNT-4A

1 VDD 2 VSS 3 SWP 4 OUT

### S-5712E Series

HIGH-SPEED LOW VOLTAGE OPERATION **OMNIPOLAR / UNIPOLAR DETECTION TYPE** HALL EFFECT SWITCH IC

#### Features

 Pole detection\*1: Detection of omnipolar, S pole or N pole

 Output logic\*1: Active "L", active "H"

 Output form\*1: Nch open-drain output, CMOS output

 Magnetic sensitivity\*1:  $B_{OP} = 3.0 \text{ mT typ.}$ Bop = 4.5 mT typ.

 $B_{OP} = 7.0 \text{ mT typ.}$ 

• Operating cycle (current consumption): Product with omnipolar detection

> $t_{CYCLE} = 0.10 \text{ ms} (I_{DD} = 640 \mu\text{A}) \text{ typ.}$ Product with S pole or N pole detection  $t_{CYCLE} = 0.05 \text{ ms} (I_{DD} = 640 \mu\text{A}) \text{ typ}.$

 $V_{DD} = 1.6 \text{ V to } 3.5 \text{ V}$ Ta =  $-40^{\circ}$ C to  $+85^{\circ}$ C

• Power supply voltage range: Operation temperature range:

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.

SOT-23-3 SNT-4A 1 VDD 1 VSS 2 VSS 2 VDD 3 NC 3 OUT 4 OUT

### S-5712A/B/C Series

LOW VOLTAGE OPERATION OMNIPOLAR / UNIPOLAR DETECTION TYPE HALL EFFECT SWITCH IC

#### Features

 Pole detection\*1: Detection of omnipolar, S pole or N pole

 Output logic\*1: Active "L", active "H"

 Output form\*1: Nch open-drain output, CMOS output

 Magnetic sensitivity\*1: Bop = 1.8 mT typ. $B_{OP} = 3.0 \text{ mT typ.}$ 

 $B_{OP} = 4.5 \text{ mT typ.}$  $B_{OP} = 7.0 \text{ mT typ.}$ 

• Operating cycle (current consumption)\*1: Product with omnipolar detection

 $t_{CYCLE}$  = 5.70 ms ( $I_{DD}$  = 12.0  $\mu$ A) typ.  $t_{CYCLE} = 50.50 \text{ ms (I}_{DD} = 2.0 \mu\text{A) typ.}$  $t_{CYCLE} = 204.10 \text{ ms} (I_{DD} = 1.0 \mu\text{A}) \text{ typ.}$ Product with S pole or N pole detection  $t_{CYCLE} = 6.05 \text{ ms } (I_{DD} = 6.0 \mu\text{A}) \text{ typ.}$  $t_{CYCLE} = 50.85 \text{ ms (I}_{DD} = 1.4 \mu\text{A) typ.}$ 

 $t_{CYCLE} = 204.05 \text{ ms } (I_{DD} = 1.0 \mu\text{A}) \text{ typ.}$ • Power supply voltage range:  $V_{DD} = 1.6 \text{ V to } 3.5 \text{ V}$  Operation temperature range: Ta =  $-40^{\circ}$ C to  $+85^{\circ}$ C

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.



### S-5717 Series

LOW VOLTAGE OPERATION **BOTH POLES / UNIPOLAR DETECTION TYPE HALL IC** 

#### Features

Pole detection\*1

 Output form<sup>\*1</sup>: • Magnetic sensitivity:

Detection logic for magnetism\*1:

• Operating cycle (current consumption)\*1:

• Power supply voltage range: · Operation temperature range:

Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.

Detection of both poles. S pole or N pole

Active "L", active "H"

Nch open-drain output, CMOS output  $B_{OP} = 3.3 \text{ mT typ.}$ 

Product with both poles detection

 $t_{CYCLE} = 50.50 \text{ ms } (I_{DD} = 2.0 \mu\text{A}) \text{ typ.}$ Product with S pole or N pole detection  $t_{CYCLE}$  = 50.85 ms ( $I_{DD}$  = 1.4  $\mu$ A) typ.

 $V_{DD} = 1.6 \text{ V to } 3.6 \text{ V}$ Ta = -40°C to +85°C

SNT-4A

1 OUT 2 VSS 3 NC 4 VDD

### S-5715 Series

#### HIGH-SPEED / MIDDLE-SPEED LOW CURRENT CONSUMPTION BOTH POLES / UNIPOLAR DETECTION TYPE HALL IC

#### Features

 Pole detection\*1. Detection of both poles, S pole or N pole

 Detection logic for magnetism\*1: Active "L". active "H"

 Output form\*1 Nch open-drain output, CMOS output

 Magnetic sensitivity:  $B_{OP} = 3.0 \text{ mT typ.}$ 

 Operating cycle (current consumption)\*1: Product with both poles detection

> $t_{CYCLE} = 0.10 \text{ ms} (1400 \mu\text{A}) \text{ typ}.$  $t_{CYCLE} = 0.90 \text{ ms } (155 \mu\text{A}) \text{ typ.}$  $t_{CYCLF} = 5.70 \text{ ms } (26 \mu\text{A}) \text{ typ.}$ Product with S pole or N pole detection

 $t_{CYCLE} = 0.05 \text{ ms } (1400 \mu\text{A}) \text{ typ.}$  $t_{CYCLF}$  = 1.25 ms (60  $\mu$ A) typ.  $t_{CYCLE}$  = 6.05 ms (13  $\mu$ A) typ.

• Power supply voltage range:  $V_{DD} = 2.7 \text{ V to } 5.5 \text{ V}$ 

• Operation temperature range: Ta = -40°C to +85°C

• Lead-free (Sn 100%), halogen-free\*2

\*1. The option can be selected.

\*2. Refer to "■ Product Name Structure" for details.



### S-5716 Series

#### LOW CURRENT CONSUMPTION **OMNIPOLAR / UNIPOLAR DETECTION TYPE** HALL EFFECT SWITCH IC

#### Features

 Pole detection\*1: Detection of omnipolar, S pole or N pole

 Output logic\*1: Active "L". active "H"

 Output form\*1: Nch open-drain output, CMOS output

 Magnetic sensitivity\*1:  $B_{OP} = 1.8 \text{ mT tvp.}$  $B_{OP} = 3.0 \text{ mT typ.}$  $B_{OP} = 3.4 \text{ mT typ.}$  $B_{OP} = 4.5 \text{ mT typ.}$ 

 $B_{OP} = 7.0 \text{ mT typ.}$ • Operating cycle (current consumption): Product with omnipolar detection

> $t_{CYCLE} = 50.50 \text{ ms } (I_{DD} = 4.0 \mu\text{A}) \text{ typ.}$ Product with S pole or N pole detection  $t_{CYCLE} = 50.85 \text{ ms } (I_{DD} = 2.6 \mu\text{A}) \text{ typ.}$

· Power supply voltage range:  $V_{DD} = 2.7 \text{ V to } 5.5 \text{ V}$  Operation temperature range:  $Ta = -40^{\circ}C$  to  $+85^{\circ}C$ 

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.



### S-57B1 Series

#### 125°C OPERATION HIGH-SPEED UNIPOLAR DETECTION TYPE HALL IC

#### Features

· Pole detection:

• Detection logic for magnetism\*1:

Output form<sup>\*1</sup>:

Magnetic sensitivity\*1:

· Operating cycle:

• Power supply voltage range: Operation temperature range:

• Lead-free (Sn 100%), halogen-free

\*1. The Option can be selected.

Detection of S pole

Active "L". active "H"

Nch open drain output, CMOS output

 $B_{OP} = 3.0 \text{ mT typ.}$  $B_{OP} = 4.5 \text{ mT typ.}$ 

 $B_{OP} = 7.0 \text{ mT typ.}$  $t_{CYCLE}$  = 50  $\mu$ s typ.

 $V_{DD} = 2.7 \text{ V to } 5.5 \text{ V}$  $Ta = -40^{\circ}C \text{ to } +125^{\circ}C$ 

> SOT-23-3 1 VSS 2 VDD 3 OUT

### S-5724 Series

#### **LOW VOLTAGE OPERATION** HIGH-SPEED BIPOLAR HALL EFFECT LATCH IC

#### Features

 Pole detection: Bipolar latch

• Output logic\*1: V<sub>OUT</sub> = "L" at S pole detection V<sub>OUT</sub> = "H" at S pole detection

• Output form\*1: Nch open-drain output, CMOS output

 Magnetic sensitivity:  $B_{OP} = 3.0 \text{ mT typ.}$ 

• Operating cycle (current consumption)\*1:  $t_{CYCLE} = 50 \mu s (I_{DD} = 640.0 \mu A) typ.$ 

 $t_{CYCLE}$  = 1.25 ms ( $I_{DD}$  = 26.0  $\mu$ A) typ.

 $t_{CYCLE}$  = 6.05 ms ( $I_{DD}$  = 6.0  $\mu$ A) typ.

• Power supply voltage range:  $V_{DD} = 1.6 \text{ V to } 3.5 \text{ V}$ • Operation temperature range:  $Ta = -40^{\circ}C \text{ to } +85^{\circ}C$ 

• Built-in power-down circuit: Extends battery life (only SNT-4A)

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.



### S-5725 Series

#### HIGH-SPEED BIPOLAR HALL EFFECT LATCH IC

#### Features

• Pole detection: Bipolar latch

 Output logic\*1: V<sub>OUT</sub> = "L" at S pole detection V<sub>OUT</sub> = "H" at S pole detection

 Output form\*1: Nch open-drain output, CMOS output

 Magnetic sensitivity\*1:  $B_{OP} = 0.8 \text{ mT typ.}$ 

> $B_{OP} = 1.8 \text{ mT typ.}$  $B_{OP} = 3.0 \text{ mT typ.}$  $B_{OP} = 7.0 \text{ mT typ.}$

• Operating cycle (current consumption)\*1:  $t_{CYCLE} = 50 \mu s (I_{DD} = 1400.0 \mu A) typ.$ 

 $t_{CYCLE} = 1.25 \text{ ms } (I_{DD} = 60.0 \mu\text{A}) \text{ typ.}$  $t_{\text{CYCLE}}$  = 6.05 ms (I<sub>DD</sub> = 13.0  $\mu$ A) typ.

• Power supply voltage range:  $V_{DD} = 2.7 \text{ V to } 5.5 \text{ V}$ 

• Operation temperature range: Ta = -40°C to +85°C

• Built-in power-down circuit: Extends battery life (only SNT-4A)

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.

SOT-23-3 SNT-4A 1 VDD 1 VSS 2 VSS 2 VDD 3 CE 3 OUT 4 OUT

### S-57M1 Series

#### HIGH-SPEED BIPOLAR HALL EFFECT LATCH

#### Features

 Pole detection: Bipolar latch

• Output logic\*1: V<sub>OUT</sub> = "L" at S pole detection V<sub>OUT</sub> = "H" at S pole detection

• Output form\*1: Nch open-drain output, CMOS output

• Magnetic sensitivity:  $B_{OP} = 3.0 \text{ mT typ.}$ 

• Operation cycle (current consumption):  $t_{CYCLE} = 50 \,\mu s \,(1400 \,\mu A) \,typ.$ • Power supply voltage range:  $V_{DD} = 2.7 \text{ V to } 5.5 \text{ V}$  $Ta = -40^{\circ}C \text{ to } +125^{\circ}C$ 

• Operation temperature range: • Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.



### S-575D B Series

125°C OPERATION. HIGH-WITHSTAND VOLTAGE, HIGH-SPEED,
OMNIPOLAR DETECTION TYPE HALL EFFECT SWITCH IC

#### Features

 Pole detection: Omnipolar detection Output logic\*1: Active "L" Active "H'

 Output form<sup>\*1</sup>: Nch open-drain output

Nch driver + built-in pull-up resistor (1.2 k $\Omega$  typ.)

 Magnetic sensitivity\*1:  $B_{OP} = 3.0 \text{ mT typ.}$ 

 $B_{OP} = 6.0 \text{ mT typ.}$  $B_{OP} = 10.0 \text{ mT typ.}$ Bop = 15.0 mT tvp.

· Chopping frequency:  $f_C = 500 \text{ kHz typ.}$ · Output delay time:  $t_D = 16.0 \, \mu s \, typ.$ • Power supply voltage range\*2: V<sub>DD</sub> = 2.7 V to 26.0 V

· Built-in regulator

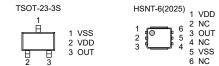
· Built-in output current limit circuit

• Operation temperature range: Ta = -40°C to +125°C

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.

\*2.  $V_{DD}$  = 2.7 V to 5.5 V when output form is Nch driver + built-in pull-up resistor (1.2 k $\Omega$  typ.)



### S-575S/5N B Series

125°C OPERATION. HIGH-WITHSTAND VOLTAGE, HIGH-SPEED, UNIPOLAR DETECTION TYPE HALL EFFECT SWITCH IC

#### Features

 Pole detection: Unipolar detection Output logic\*1 Active "L" Active "H"

 Output form\*1: Nch open-drain output

Nch driver + built-in pull-up resistor (1.2 kΩ typ.)

 Magnetic sensitivity\*1:  $B_{OP} = 3.0 \text{ mT typ.}$ 

Bop = 6.0 mT typ. $B_{OP} = 10.0 \text{ mT typ.}$  $B_{OP} = 15.0 \text{ mT typ.}$ 

· Chopping frequency:  $f_C = 500 \text{ kHz typ.}$  Output delay time:  $t_D = 8.0 \mu s typ.$  Power supply voltage range\*2:  $V_{DD} = 2.7 \text{ V to } 26.0 \text{ V}$ 

· Built-in regulator

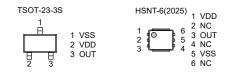
. Built-in output current limit circuit

• Operation temperature range: Ta = -40°C to +125°C

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.

\*2.  $V_{DD}$  = 2.7 V to 5.5 V when output form is Nch driver + built-in pull-up resistor (1.2 k $\Omega$  typ.)



### S-5733 B Series

125°C OPERATION. HIGH-WITHSTAND VOLTAGE, HIGH-SPEED, UNIPOLAR DETECTION TYPE HALL EFFECT SWITCH IC

#### Features

 Pole detection: Detection of S pole Output logic\*1: Active "L" Active "H" • Output form\*1:

Nch open-drain output

Nch driver + built-in pull-up resistor (1.2 k $\Omega$  typ.)

 Magnetic sensitivity\*1:  $B_{OP} = 10.0 \text{ mT typ.}$  $B_{OP} = 15.0 \text{ mT typ.}$  Chopping frequency  $f_C = 500 \text{ kHz typ.}$  $t_D = 8.0 \mu s typ.$ · Output delay time: Power supply voltage range\*2:  $V_{DD} = 2.7 \text{ V to } 26.0 \text{ V}$ 

· Built-in regulator

· Built-in output current limit circuit

• Operation temperature range: Ta = -40°C to +125°C

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected

\*2.  $V_{DD}$  = 2.7 V to 5.5 V when output form is Nch driver + built-in pull-up resistor (1.2 k $\Omega$  typ.)



### S-5732 B Series

125°C OPERATION HIGH-WITHSTAND VOLTAGE HIGH-SPEED UNIPOLAR DETECTION TYPE HALL EFFECT SWITCH IC

#### Features

 Pole detection\*1 Detection of S pole Detection of N pole Output logic\*1 Active "L"

Active "H" Output form\*1: Nch open-drain output

Nch driver + built-in pull-up resistor Magnetic sensitivity\*1:  $B_{OP} = 3.0 \text{ mT typ.}$ 

 $B_{OP} = 6.0 \text{ mT typ.}$ · Chopping frequency:  $f_C = 250 \text{ kHz typ.}$ · Output delay time:  $t_D = 16.0 \mu s typ.$  $V_{DD} = 3.5 \text{ V to } 26.0 \text{ V}$ 

Power supply voltage range:

Built-in regulator

· Built-in output current limit circuit · Operation temperature range:

• Lead-free (Sn 100%), halogen-free

Ta = -40°C to +125°C

\*1. The option can be selected



### S-5732 I Series

## HIGH-WITHSTAND VOLTAGE HIGH-SPEED UNIPOLAR DETECTION TYPE HALL EFFECT SWITCH IC

#### Features

Pole detection\*1:

Output logic\*1:

Output form<sup>\*1</sup>:

Magnetic sensitivity\*1:

Chopping frequency:Output delay time:

Power supply voltage range:

· Built-in regulator

Built-in output current limit circuit

• Operation temperature range:

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.

Detection of S pole Detection of N pole Active "L" Active "H"

Nch open-drain output

Nch driver + built-in pull-up resistor

 $B_{OP} = 3.0 \text{ mT typ.}$   $B_{OP} = 6.0 \text{ mT typ.}$   $f_{C} = 250 \text{ kHz typ.}$   $t_{D} = 16.0 \mu\text{s typ.}$  $V_{DD} = 3.5 \text{ V to } 26.0 \text{ V}$ 

Ta = -40°C to +85°C



### S-5731 Series

HIGH-WITHSTAND VOLTAGE HIGH-SPEED UNIPOLAR DETECTION TYPE HALL EFFECT SWITCH IC

#### Features

Pole detection\*1:
Output logic\*1:

• Output form\*1:

Magnetic sensitivity\*1:

Chopping frequency:Output delay time:

Power supply voltage range:

• Built-in regulator

Built-in output current limit circuit

Operation temperature range:

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.

Detection of S pole, detection of N pole

Active "L", active "H"

Nch open-drain output,

Nch driver + built-in pull-up resistor

 $B_{OP} = 3.0 \text{ mT typ.},$  $B_{OP} = 6.0 \text{ mT typ.}$ 

 $f_C = 250 \text{ kHz typ.}$  $t_D = 16.0 \text{ } \mu\text{s typ.}$ 

 $V_{DD} = 3.5 \text{ V to } 26.0 \text{ V}$ 

Ta = -40°C to +85°C

SOT-23-3

1
H
1 VSS
2 VDD
H
H
3 OUT

### S-576Z B Series

### 125°C OPERATION, HIGH-WITHSTAND VOLTAGE, HIGH-SPEED, ZERO CROSSING LATCH HALL EFFECT IC

#### Features

 Pole detection: Zero Crossing Latch detection • Output logic\*1: V<sub>OUT</sub> = "L" at S pole detection V<sub>OUT</sub> = "H" at S pole detection Output form\*1:

Nch open-drain output

Nch driver + built-in pull-up resistor (1.2 k $\Omega$  typ.)

• Zero crossing latch point:  $B_Z = 0.0 \text{ mT typ.}$ • Release point (S pole)\*1:  $B_{RS} = 3.0 \text{ mT typ.}$  $B_{RS} = 6.0 \text{ mT typ.}$ • Chopping frequency:  $f_C = 500 \text{ kHz typ.}$ • Output delay time:  $t_D = 8.0 \mu s typ.$ • Power supply voltage range\*2: V<sub>DD</sub> = 2.7 V to 26.0 V

Built-in regulator

Built-in output current limit circuit

• Operation temperature range: Ta = -40°C to +125°C

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.

\*2.  $V_{DD}$  = 2.7 V to 5.5 V when output form is Nch driver + built-in pull-up resistor (1.2 k $\Omega$  typ.)



### S-576Z R Series

**EXTENDED OPERATION TEMPERATURE RANGE,** HIGH-WITHSTAND VOLTAGE, HIGH-SPEED, ZERO CROSSING LATCH HALL EFFECT IC

#### Features

 Pole detection: Zero Crossing Latch detection • Output logic\*1: Vout = "L" at S pole detection V<sub>OUT</sub> = "H" at S pole detection

• Output form\*1: Nch open-drain output

Nch driver + built-in pull-up resistor (1.2 k $\Omega$  typ.)

• Zero crossing latch point:  $B_Z = 0.0 \text{ mT typ.}$  Release point (S pole)\*1:  $B_{RS} = 3.0 \text{ mT typ.}$  $B_{RS} = 6.0 \text{ mT typ.}$  Chopping frequency:  $f_C = 500 \text{ kHz typ.}$  Output delay time:  $t_D = 8.0 \, \mu s \, typ.$  Power supply voltage range\*2:  $V_{DD} = 2.7 \text{ V to } 26.0 \text{ V}$ 

• Built-in regulator

• Built-in output current limit circuit

• Operation temperature range: Ta = -50°C to +150°C

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.

\*2.  $V_{DD}$  = 2.7 V to 5.5 V when output form is Nch driver + built-in pull-up resistor (1.2 k $\Omega$  typ.)



### S-576B B Series

#### 125°C OPERATION, HIGH-WITHSTAND VOLTAGE, HIGH-SPEED, BIPOLAR HALL EFFECT LATCH IC

### Features

Pole detection:
 Bipolar latch

Output form\*1:
 Nch open-drain output

Nch driver + built-in pull-up resistor (1.2 k $\Omega$  typ.)

• Magnetic sensitivity\*1: Bop = 0.5 mT typ.

 $B_{OP}$  = 2.2 mT typ.  $B_{OP}$  = 3.0 mT typ.  $B_{OP}$  = 6.0 mT typ.  $B_{OP}$  = 10.0 mT typ.

• Chopping frequency:  $f_C = 500 \text{ kHz typ.}$ • Output delay time:  $t_D = 8.0 \text{ µs typ.}$ • Power supply voltage range\*2:  $V_{DD} = 2.7 \text{ V to } 26.0 \text{ V}$ 

• Built-in regulator

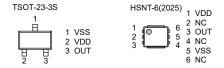
Built-in output current limit circuit

• Operation temperature range: Ta = -40°C to +125°C

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.

\*2.  $V_{DD}$  = 2.7 V to 5.5 V when output form is Nch driver + built-in pull-up resistor (1.2 k $\Omega$  typ.)



### S-5743 A Series

125°C OPERATION HIGH-WITHSTAND VOLTAGE HIGH-SPEED BIPOLAR HALL EFFECT LATCH IC

#### Features

Pole detection:
 Bipolar latch

• Output logic<sup>™</sup>: V<sub>OUT</sub> = "L" at S pole detection V<sub>OUT</sub> = "H" at S pole detection

Output form:
Output form:
Nch open-drain output
Bor = 0.5 mT typ.

 $B_{OP}$  = 1.5 mT typ.  $B_{OP}$  = 2.2 mT typ.  $B_{OP}$  = 3.0 mT typ.

Built-in regulator

Built-in output current limit circuit

• Operation temperature range: Ta = -40°C to +125°C

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.



### S-5742 B Series

125°C OPERATION HIGH-WITHSTAND VOLTAGE HIGH-SPEED BIPOLAR HALL EFFECT LATCH IC

#### Features

Pole detection:
 Bipolar latch

• Output logic  $^{\text{\tiny M}}$ :  $V_{\text{OUT}}$  = "L" at S pole detection  $V_{\text{OUT}}$  = "H" at S pole detection

Output form<sup>\*1</sup>: Nch open-drain output
 Nch driver + built-in pull-up resistor

• Magnetic sensitivity\*1: Ron arriver + built-in pull-up resistor

• Magnetic sensitivity\*1: B<sub>OP</sub> = 1.8 mT typ.

 $B_{OP} = 3.0 \text{ mT typ.}$   $B_{OP} = 6.0 \text{ mT typ.}$ 

• Chopping frequency:  $f_C = 500 \text{ kHz typ.}$ • Output delay time:  $t_D = 8.0 \text{ } \mu \text{s typ.}$ 

• Power supply voltage range:  $V_{DD} = 3.5 \text{ V to } 26.0 \text{ V}$ 

Built-in regulator

• Built-in output current limit circuit

• Operation temperature range: Ta = -40°C to +125°C

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.



### S-5742 I Series

#### HIGH-WITHSTAND VOLTAGE HIGH-SPEED BIPOLAR HALL EFFECT LATCH IC

#### Features

• Pole detection: Bipolar latch

• Output logic\*1:  $V_{\text{OUT}} = \text{"L" at S pole detection} \\ V_{\text{OUT}} = \text{"H" at S pole detection}$ 

• Output form\*1: Nch open-drain output

Nch driver + built-in pull-up resistor

 $B_{OP}$  = 1.8 mT typ.

 $B_{OP}$  = 3.0 mT typ.  $B_{OP}$  = 6.0 mT typ.

Ta =  $-40^{\circ}$ C to  $+85^{\circ}$ C

• Chopping frequency:  $f_C = 500 \text{ kHz typ.}$ • Output delay time:  $t_D = 8.0 \text{ } \mu \text{s typ.}$ 

• Power supply voltage range:  $V_{DD} = 3.5 \text{ V to } 26.0 \text{ V}$ 

• Built-in regulator

Magnetic sensitivity\*1:

• Built-in output current limit circuit

• Operation temperature range:

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.



### S-5741 B Series

125°C OPERATION HIGH-WITHSTAND VOLTAGE HIGH-SPEED BIPOLAR HALL EFFECT LATCH IC

#### Features

Pole detection:
 Bipolar latch

• Output logic\*1:  $V_{\text{OUT}} = \text{"L"}$  at S pole detection  $V_{\text{OUT}} = \text{"H"}$  at S pole detection

• Output form\*1: Nch open-drain output

Nch driver + built-in pull-up resistor

• Magnetic sensitivity\*1: B<sub>OP</sub> = 1.8 mT typ.

 $B_{OP}$  = 3.0 mT typ.  $B_{OP}$  = 6.0 mT typ.

• Chopping frequency:  $f_C = 500 \text{ kHz typ.}$ • Output delay time:  $t_D = 8.0 \text{ } \mu \text{s typ.}$ • Power supply voltage range:  $V_{DD} = 3.5 \text{ V to } 26.0 \text{ V}$ 

Built-in regulator

• Built-in output current limit circuit

• Operation temperature range: Ta = -40°C to +125°C

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.

SOT-23-3S

1 H 1 VSS 2 VDD 2 S 3 OUT

Refer to the datasheet of each product for details.

### S-5701 B Series

125°C OPERATION, SUPER LOW CURRENT CONSUMPTION, LOW VOLTAGE OPERATION, OMNIPOLAR DETECTION TYPE TMR MAGNETIC SENSOR IC

#### Features

• Detection direction: Horizontal direction

(Refer to "■ Operation" for details)

 Pole detection: Omnipolar detection

• Output logic: Active "L" • Output form: CMOS output Magnetic sensitivity\*1:  $B_{OP} = 1.0 \text{ mT typ.}$  $B_{OP} = 3.0 \text{ mT typ.}$ 

• Operating cycle (current consumption):  $t_{CYCLE} = 100 \text{ ms (IDD} = 160 \text{ nA) typ.}$ 

• Power supply voltage range:  $V_{DD} = 1.7 \text{ V to } 5.5 \text{ V}$  Operation temperature range:  $Ta = -40^{\circ}C \text{ to } +125^{\circ}C$ 

• Lead-free (Sn 100%), halogen-free

\*1. The option can be selected.

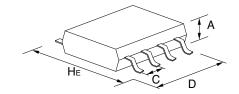
TSOT-23-3S



Package Type	Pin	Pin	Package Size (mm)			Pitch (mm)
	Count Package Name	HE	D	A (max.)	С	
Lead insertion type	3	TO-92	7.0	5.2	4.2	2.5/1.27
	3	TO-92S	4.95	4.1	1.62	2.5/1.27
Flat-lead type	3	SOT-89-3	4.0	4.5	1.6	1.5
	5	SOT-89-5	4.5	4.5	1.6	1.5
Gull-wing type	4	SC-82AB	2.1	2.0	1.1	1.3
	5	SC-88A	2.1	2.0	1.1	0.65
	3	SOT-23-3	2.8	2.9	1.3	1.9
	3	SOT-23-3S	2.8	2.9	1.2	1.9
	3	TSOT-23-3S	2.85	2.9	0.8	1.9
	5	SOT-23-5	2.8	2.9	1.3	0.95
	6	SOT-23-6	2.8	2.9	1.35	0.95
	6	SOT-23-6W	2.8	2.9	1.3	0.95
	8	8-Pin SOP (JEDEC)	6.0	5.02	1.75	1.27
	8	8-Pin TSSOP	6.4	3.0	1.1	0.65
	8	8-Pin TSSOP	6.4	3.0	1.1	0.65
	16	16-Pin TSSOP	6.4	5.1	1.1	0.65
	20	20-Pin TSSOP	6.4	6.5	1.2	0.65
	24	24-Pin SSOP	7.6	7.9	1.4	0.65
	8	TMSOP-8	4.0	2.9	0.8	0.65
	8	HTMSOP-8	4.0	2.9	0.8	0.65
	16	HTSSOP-16	6.4	5.12	1.1	0.65
	6	HSOP-6	6.0	5.02	1.75	1.91
	8	HSOP-8A	6.0	5.02	1.68	1.27
	8	HSOP-8A	6.0	5.02	1.65	1.27
	8	HSOP-8Q	6.0	5.02	1.68	1.27
	5	TO-252-5S(A)	6.5	6.5	1.4	1.27
	9	TO-252-9S	6.5	6.5	1.4	0.65

Package Type	Pin Count	Dackago Nama	Package Size (mm)			Pitch (mm)
		Package Name	HE	D	A (max.)	С
Non-lead type	6	6-Pin HSON(A)	3.0	2.9	0.9	0.95
	6	SON-6C	2.55	1.56	0.65	0.5
	4	SNT-4A	1.6	1.2	0.5	0.65
	6	SNT-6A SNT-6A(H)	1.8	1.57	0.5	0.5
	8	SNT-8A	2.46	1.97	0.5	0.5
	4	HSNT-4(0808)	0.8	0.8	0.4	0.4
	4	HSNT-4(0808)B	0.8	0.8	0.41	0.4
	4	HSNT-4(1010)	1.0	1.0	0.4	0.65
	4	HSNT-4(1010)B	1.0	1.0	0.41	0.65
	6	HSNT-6(1212)	1.2	1.2	0.4	0.4
	6	HSNT-6A	2.46	1.96	0.5	0.5
	6	HSNT-6(2025)	2.46	1.96	0.5	0.5
	8	HSNT-8(1616)	1.6	1.6	0.4	0.4
	8	HSNT-8(2030)	3.0	2.0	0.5	0.5
	6	DFN-6(1414)A	1.4	1.4	0.6	0.5
	6	DFN-6(1518)A	1.8	1.5	0.33	0.5
	8	DFN-8(1616)A	1.6	1.6	0.6	0.4
	8	DFN-8(2030)	3.0	2.0	0.5	0.5
	8	DFN-8(2030)A	3.0	2.0	0.6	0.5
	8	DFN-8(2030)B	3.0	2.0	0.8	0.5

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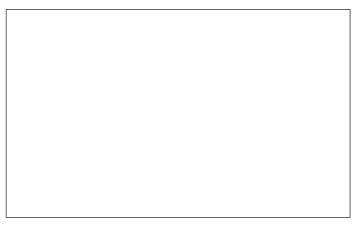
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